

Notice of References Cited	Application/Control No. 10/044,487	Applicant(s)/Patent Under Reexamination PARADY, BODO K.	
	Examiner David J. Huisman	Art Unit 2183	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,933,627	08-1999	Parady, Bodo	712/228
	B	US-5,822,788	10-1998	Kahn et al.	711/213
	C	US-5,377,336	12-1994	Eickemeyer et al.	712/207
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	J	US-			
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Yoaz et al., "Speculation Techniques for Improving Load Related Instruction Scheduling," May 1999, pp.42-53			
	V	Chen et al., "Effective Hardware-Based Data Prefetching for High-Performance Processors," 1993, pp.609-623			
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.